

# IDS-C6xxALB-yzzR

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## 6.144Gbps LC Receptacle Duplex CWDM SFP+ Transceiver

### Features

- 18-Wavelength CWDM Transmitter from 1271nm to 1611nm, with step 20nm
- Standard Small Form Pluggable package with receptacle LC/UPC connector
- Compliant with SFP+ MSA
- SFF-8472 compliant digital diagnostic monitoring function implemented
- Internally calibrated mode
- Uncooled DFB lasers Transmitter
- High sensitive PIN-PD/APD Receiver
- Single +3.3V power supply
- Operating case temperature; 0°C to +70°C (-40°C to +85°C)
- Differential CML inputs and outputs
- Internally AC-Coupled electrical interface
- RoHS compliant

### Description

IDS-C6xxALB-yzzR SFP+ transceivers are designed to meet serial optical data communications specification. The transceivers are manufactured in hot pluggable capability package with receptacle LC connector interface and made of metallized housing to obtain excellent EMI shielding.

The transmitter consists of DFB laser in an optical subassembly (OSA).

The OSA is driven by a custom IC which converts differential CML logic signals into a laser diode drive current. The receiver includes a planar InGaAs PIN-PD/ APD mounted with a transimpedance preamplifier IC in an OSA. The OSA is mated to a custom limiting-amplifier which provides post-amplification and signal detect function (Logic 0 indicates normal operation).

The transceivers are designed to used in a single power supply (+3.3V) and an operating temperature range of -40°C to +85°C

### Applications

- Digital Wireless Repeaters
- Digital Wireless BTS Interconnects: OBSAI and CPRI Standards
- High-speed data links.
- Other optical links

# IDS-C6xxALB-yzzR

## Absolute Maximum Ratings

Parameter	Symbol	Min	Typ	Max	Unit
Storage Temperature	T <sub>ST</sub>	-40		+85	°C
Power Supply Voltage	V <sub>CC</sub>	0		3.6	V
Operating Humidity	H <sub>OP</sub>			85	% RH

## Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Unit
Operating Case Temperature	T <sub>OP</sub>	0(T <sub>A</sub> )		70(T <sub>C</sub> )	°C
	T <sub>OP</sub>	-40(T <sub>A</sub> )		85(T <sub>C</sub> )	°C
Supply Voltage	V <sub>CC</sub>	3.15	3.3	3.45	V
Power Supply Current	I <sub>CC(Tx+Rx)</sub>			430	mA

## CWDM Wavelength

Parameter	Symbol	Min	Typ	Max	Unit
O-band Original	λ	1264.5	1271	1277.5	nm
	λ	1284.5	1291	1297.5	nm
	λ	1304.5	1311	1317.5	nm
	λ	1324.5	1331	1337.5	nm
	λ	1344.5	1351	1357.5	nm
E-band Extended	λ	1364.5	1371	1377.5	nm
	λ	1384.5	1391	1397.5	nm
	λ	1404.5	1411	1417.5	nm
	λ	1424.5	1431	1437.5	nm
	λ	1444.5	1451	1457.5	nm
S-band Short Wavelength	λ	1464.5	1471	1477.5	nm
	λ	1484.5	1491	1497.5	nm
	λ	1504.5	1511	1517.5	nm
	λ	1524.5	1531	1537.5	nm
C-band Conventional	λ	1544.5	1551	1557.5	nm
L-band Long Wavelength	λ	1564.5	1571	1577.5	nm
	λ	1584.5	1591	1597.5	nm
	λ	1604.5	1611	1617.5	nm

# IDS-C6xxALB-yzzR

## Transmitter Characteristics\_IDS-C6xxALB-y10R

Parameter	Symbol	Min	Typ	Max	Unit
Bit rate	B	0.622	6.144	6.25	Gbps
Center Wavelength	$\lambda_C$	$\lambda_C - 6.5$	$\lambda_C$	$\lambda_C + 6.5$	nm
Output Spectral Width (-20dB)	$\Delta\lambda$			1	nm
Average Launch Power	$P_o$	-4		+1	dBm
Extinction ratio	dB	4	5		dB
Side Mode Suppression Ratio	SMSR	30			dB
Differential data input swing	$V_{IN, p-p}$	200		1000	mV
TX_Disable Input High Voltage	$V_{IH}$	2.4			V
TX_Disable Input Low Voltage	$V_{IL}$			0.8	V
TX_Fault Output High Voltage	$V_{OH}$	Host Vcc - 0.5		Host Vcc	V
TX_Fault Output Low Voltage	$V_{OL}$	0		0.4	V
Dispersion Penalty	TDP			2.0	dB
Average Launch Power of Off TX	$P_{o\_OFF}$			-30	dBm

## Transmitter Characteristics\_IDS-C6xxALB-y14R

Parameter	Symbol	Min	Typ	Max	Unit
Bit rate	B	0.622	6.144	6.25	Gbps
Center Wavelength	$\lambda_C$	$\lambda_C - 6.5$	$\lambda_C$	$\lambda_C + 6.5$	nm
Output Spectral Width (-20dB)	$\Delta\lambda$			1	nm
Average Launch Power	$P_o$	0		5	dBm
Extinction ratio	dB	4	5		dB
Side Mode Suppression Ratio	SMSR	30			dB
Differential data input swing	$V_{IN, p-p}$	200		1000	mV
TX_Disable Input High Voltage	$V_{IH}$	2.4			V
TX_Disable Input Low Voltage	$V_{IL}$			0.8	V
TX_Fault Output High Voltage	$V_{OH}$	Host Vcc - 0.5		Host Vcc	V
TX_Fault Output Low Voltage	$V_{OL}$	0		0.4	V
Dispersion Penalty	TDP			2.0	dB
Average Launch Power of Off TX	$P_{o\_OFF}$			-30	dBm

# IDS-C6xxALB-yzzR

## Transmitter Characteristics\_IDS-C6xxALB-y22R

Parameter	Symbol	Min	Typ	Max	Unit
Bit rate	B	0.622	6.144	6.25	Gbps
Center Wavelength	$\lambda_C$	$\lambda_C - 6.5$	$\lambda_C$	$\lambda_C + 6.5$	nm
Output Spectral Width (-20dB)	$\Delta\lambda$			1	nm
Average Launch Power	$P_o$	1		6	dBm
Extinction ratio	dB	4	5		dB
Side Mode Suppression Ratio	SMSR	30			dB
Differential data input swing	$V_{IN, p-p}$	200		1000	mV
TX_Disable Input High Voltage	$V_{IH}$	2.4			V
TX_Disable Input Low Voltage	$V_{IL}$			0.8	V
TX_Fault Output High Voltage	$V_{OH}$	Host Vcc - 0.5		Host Vcc	V
TX_Fault Output Low Voltage	$V_{OL}$	0		0.4	V
Dispersion Penalty	TDP			2.0	dB
Average Launch Power of Off TX	$P_{o\_OFF}$			-30	dBm

# IDS-C6xxALB-yzzR

## Receiver Characteristics\_IDS-C6xxALB-y10R/y14R

Parameter	Symbol	Min	Typ	Max	Unit
Bit rate	B	0.622	6.144	6.25	Gbps
Received Wavelength	$\lambda_C$	1260		1620	nm
Differential data output swing	$V_{OUT, p-p}$	200		800	mV
RX_LOS Output Voltage-high	$V_{OH}$	Host Vcc - 0.5		Host Vcc	V
RX_LOS Output Voltage-Low	$V_{OL}$	0		0.4	V
Average Rx Sensitivity (Note1)	$P_{min}$			-14	dBm
Maximum Input Power	$P_{max}$	0			dBm
LOS De-Assert	$LOS_D$			-15	dBm
LOS Assert	$LOS_A$	-35			dBm
LOS Hysteresis	$LOS_{HYS}$	0.5			dB

Note 1) Sensitivity and saturation level at PRBS2<sup>23</sup>-1, @6.144Gbps, BER 1x10<sup>-12</sup>.

## Receiver Characteristics\_IDS-C6xxALB-y22R

Parameter	Symbol	Min	Typ	Max	Unit
Bit rate	B	0.622	6.144	6.25	Gbps
Received Wavelength	$\lambda_C$	1260		1620	nm
Differential data output swing	$V_{OUT, p-p}$	200		800	mV
RX_LOS Output Voltage-high	$V_{OH}$	Host Vcc - 0.5		Host Vcc	V
RX_LOS Output Voltage-Low	$V_{OL}$	0		0.4	V
Average Rx Sensitivity (Note1)	$P_{min}$			-21	dBm
Maximum Input Power	$P_{max}$	-8			dBm
LOS De-Assert	$LOS_D$			-22	dBm
LOS Assert	$LOS_A$	-40			dBm
LOS Hysteresis	$LOS_{HYS}$	0.5			dB

Note 1) Sensitivity and saturation level at PRBS2<sup>23</sup>-1, @6.144Gbps, BER 1x10<sup>-12</sup>.

# IDS-C6xxALB-yyyR

## PIN description

These devices can be installed in or removed from any MSA-compliant Pluggable Small Form Factor port regardless of whether the host equipment is operating or not. These devices are simply inserted, electrical-interface first, under finger-pressure. Controlled hot-plugging is ensured by 3-stage pin sequencing at the electrical interface. See the Fig. 1. As these devices are inserted, first contact is made by the housing ground shield, discharging any potentially component-damaging static electricity. Ground pins engage next and are followed by TX and RX power supplies. Finally, signal pins are connected.

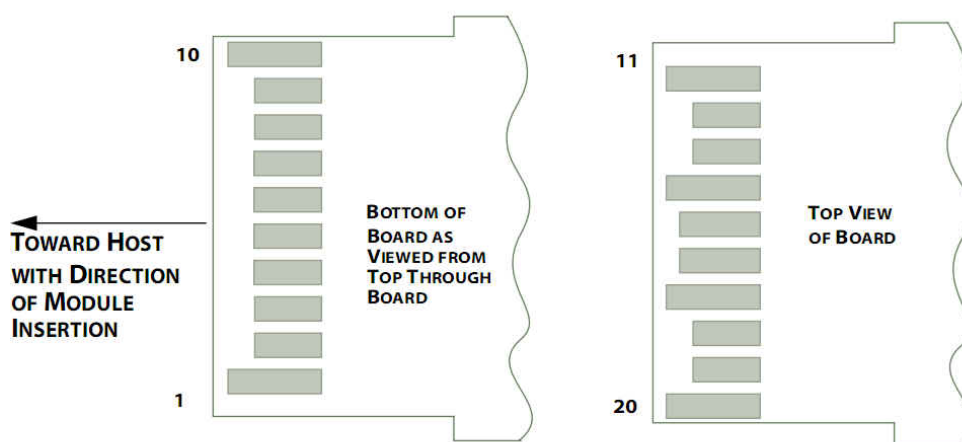


Fig. 1 SFP+ Transceiver Electrical Pad Layout

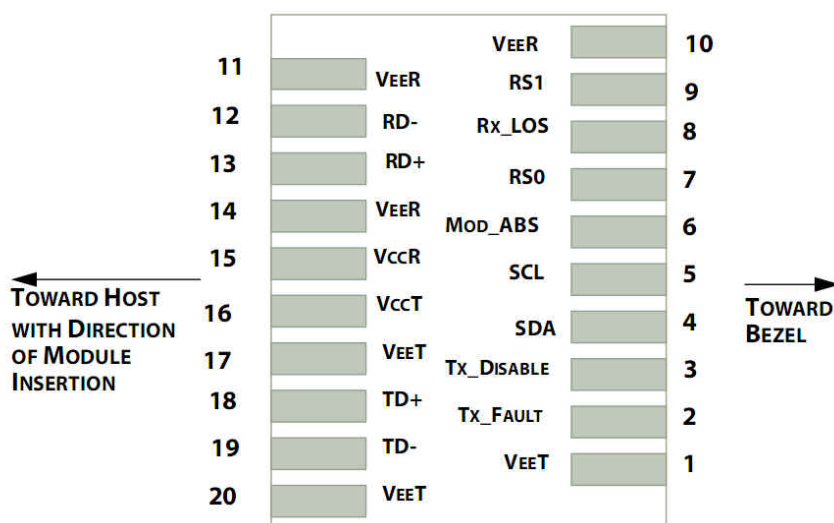


Fig. 2 Host PCB SFP+ Pinout

# IDS-C6xxALB-yzzR

## PIN assignment

Pin	Name	In/Out	Logic	Description	Note
1	VeeT			Module Transmitter Ground	
2	TX_Fault		LVTTTL	Module Transmitter Fault	2
3	TX_Dis	I	LVTTTL	Transmitter Disable; Turns off transmitter laser output	3
4	SDA	I/O	LVTTTL	2-Wire Serial Interface Data Line	
5	SCL	I/O	LVTTTL	2-Wire Serial Interface Clock	
6	Mod_ABS			Module Absent, connected to VeeT or VeeR in the module	2
7	RS0	I	LVTTTL	Rate Select 0 (not functional for 10GE type)	
8	RX_LOS	O	LVTTTL	Receiver Loss Of Signal Indication (In FC designated as RX_LOS and in Ethernet designated as Signal Detect)	2
9	RS1	I	LVTTTL	Rate Select 1 (not functional for 10GE type)	
10	VeeR			Module Receiver Ground	1
11	VeeR			Module Receiver Ground	1
12	RD-	O	CML	Receiver Inverted Data Output	
13	RD+	O	CML	Receiver Non-Inverted Data Output	
14	VeeR			Module Receiver Ground	1
15	VccR			Module Receiver 3.3 V Supply	
16	VccT			Module Transmitter 3.3 V Supply	
17	VeeT			Module Transmitter Ground	1
18	TD+	I	CML	Transmitter Non-Inverted Data Input	
19	TD-	I	CML	Transmitter Inverted Data Input	
20	VeeT			Module Transmitter Ground	1

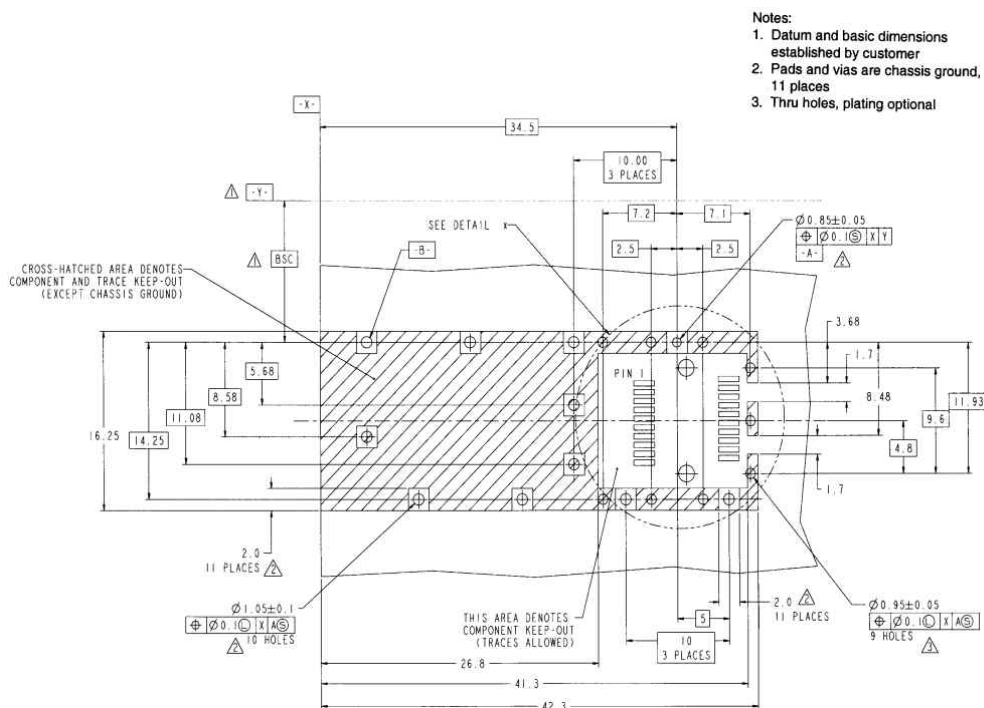
### Note

- 1: Module ground pins are isolated from the module case and chassis ground within the module.
- 2: Shall be pulled up with 4.7k to 10k ohm to a voltage between 3.15V and 3.45V on the host board.
- 3: Shall be pulled up with 4.7k to 10k ohm to VccT in the module.

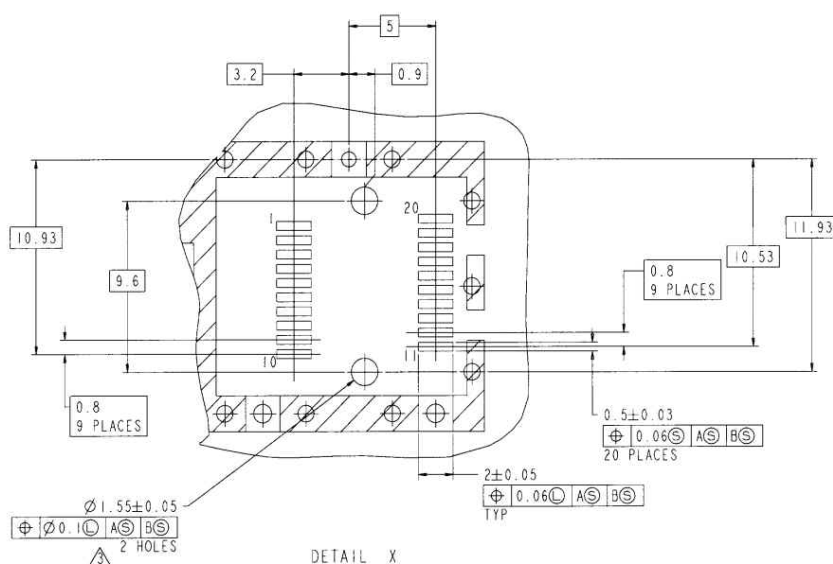
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## SFP+ Host Board Mechanical Layout [unit: mm]

A typical host board mechanical layout for attaching the SFP+ connector and cage system is shown in Fig. 3 and Fig. 4.



**Fig. 3 Host board mechanical layout (mm)**



**Fig. 4 Detailed host board mechanical layout (mm)**



# IDS-C6xxALB-yzzR

## Host Board Power Supply filtering

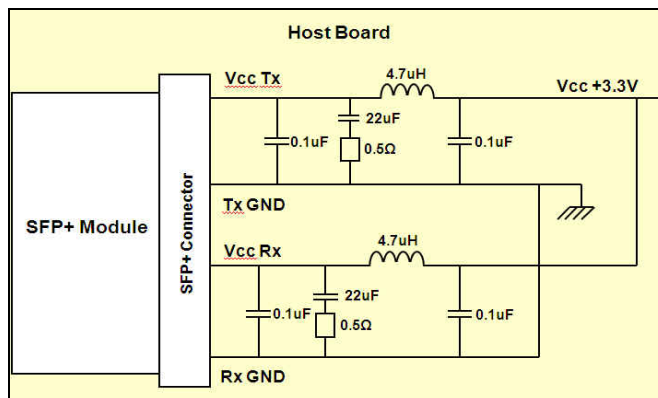


Fig. 5 Host board power supply filtering

## Recommended Circuit Schematic

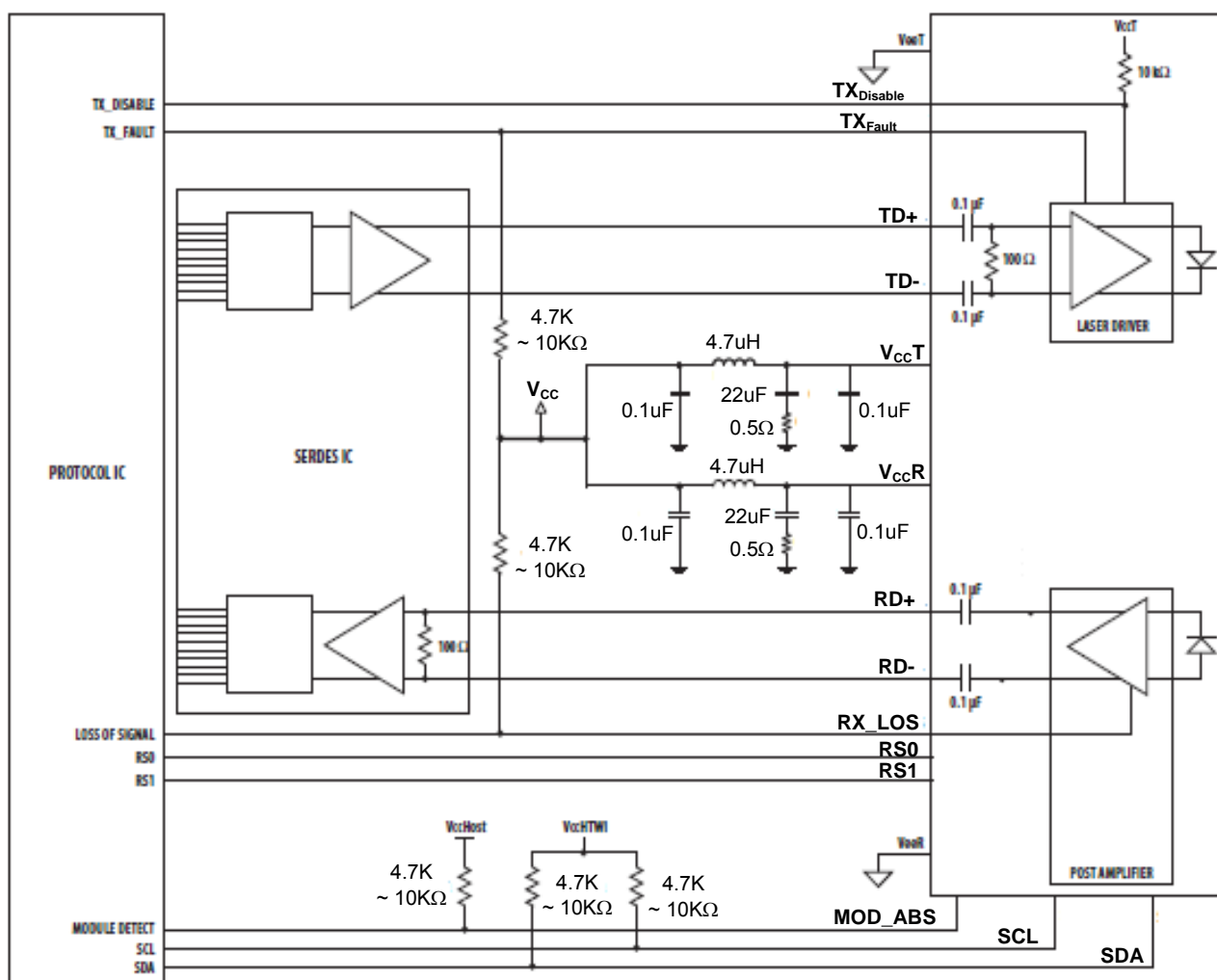


Fig. 6 SFP+ Host board Schematic Application

# IDS-C6xxALB-yzzR

## Digital Diagnostic Functions

The IDS-C6xxALB-yzzR SFP+ transceivers support the 2-wire serial communication protocol (I<sup>2</sup>C) as defined in the SFP MSA. Through this serial communication, these transceivers provide access to identification information that describes their capabilities, standard interfaces, manufacturer, and other information. In addition, these SFP transceivers provide enhanced digital diagnostic monitoring interface, which allows real-time access to device operating conditions such as internal temperature, laser bias current, transmitted optical power, received optical power and supply voltage. It also defines a sophisticated system of alarm and warning flags.

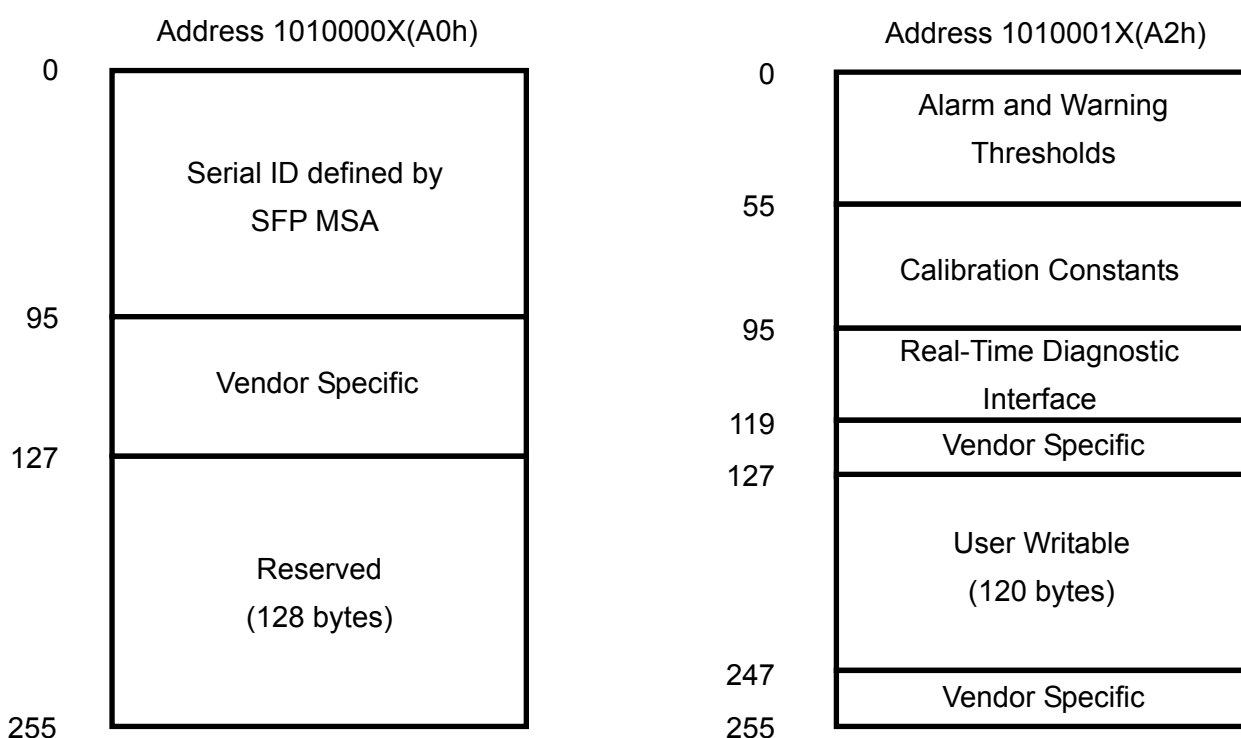


Fig. 7 Two-wire serial digital memory map

# IDS-C6xxALB-yzzR

## Ordering Information

For more information on this or other products and their availability, please contact e-mail at sales@intecec.com.

①	②	-	③	④	⑤	⑥	-	⑦	⑧	⑨
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No.	ITEM	Code	Description
①	Company	I	INTEC E&C
②	Form Factor	DS	Duplex SFP+
③	Data-rate	C6	6.144Gbps
④	Tx wavelength	xx	27(1270nm) ~ 61(1610nm)
⑤	Rx wavelength	AL	ALL Wavelength
⑥	Optical interface	B	LC-UPC receptacle
⑦	Temperature range	y	C: 0°C ~ 70°C, I: -40°C ~ 85°C
⑧	Link budget	zz	10/14/22dB,
⑨	RoHS	R	RoHS compliant

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